

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i> <i>Express Mail Number EV681354931US</i>				Docket Number (Optional) 100-19811 (P05402-D01-F1)		Application Number 10/772,863	
				Applicant(s) Abdalla Aly Naem, et al.			
				Filing Date February 4, 2004		Group Art Unit 2823	
APR 19 2006 U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
WB		*6,103,603	08/15/00	Han	438	532	04/24/98
WB		*6,512,252 B1	01/28/03	Takagi, et al.	257	192	11/15/00
U.S. PATENT APPLICATION PUBLICATIONS							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
WB		*2002/0179946 A1	12/05/02	Hara, et al.	257	288	04/18/01
FOREIGN PATENT DOCUMENTS							
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation
							YES
							NO
OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>							
WB		Stanley Wolf, et al., "Silicon Processing For The VLSI Era", Volume 1, Processing Technology, Lattice Press, 1986, pp. 155.					
EXAMINER /William Brewster/				DATE CONSIDERED 18 May 2006			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							